



UNIVERSITY SCHOOL OF AUTOMATION AND ROBOTICS (USAR)

GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY

"A STATE UNIVERSITY ESTABLISHED BY THE GOVT. OF NCT OF DELHI"

East Delhi Campus, Surajmal Vihar, Delhi - 110 092

Email Id: dean.usar@ipu.ac.in

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F. 58-3/2024/USAR/Ph.D./MainFile/3275^L

18th December 2024

Sub: List of PET-exempted candidates for admission to Ph. D programme (2024 winter session) in USAR.

With reference to 58-4(2)/USAR/Ph.D./Misc/2024/3027 dated 29.11.2024, following applicants are exempted from the Ph.D. Entrance Test (PET) - 2024 for admission to winter session of the academic session 2024-2025 in USAR.

Sr. No.	Name	Category	Discipline	Basis of Qualification of PET Exemption	Discipline of JRF/NET/GATE
1	Sree Vrinda G M	Gen	AIDS	UGC NET Category 3	Computer Science & App
2	Arvind Kumar Sharma	OBC (NCL)	AIML	UGC NET Category 3	Computer Science & App
3	Sakshi	GEN-EWS	AIML	UGC NET Category 2	Computer Science & App
4	Hari Singh Rajpoot	OBC (NCL)	AIML	UGC NET Category 3	Computer Science & App
5	Vivek Kumar Jha	Gen	AIML	UGC NET Category 3	Computer Science & App
6	Nikita	OBC (NCL)	AIML	UGC NET Category 2	Computer Science & App
7	Kundan Pawar	Gen	AIML	UGC JRF	Computer Science & App
8	Himani	OBC (NCL)	AIML	UGC NET Category 2	Computer Science & App
9	Sonia Ahlawat	SC	IIT	UGC JRF	Computer Science & App
10	Neeraj Singh	SC	AIML	UGC NET Category 2	Computer Science & App

For interview schedule candidates are advised to visit the university website www.ipu.ac.in regularly. Eligibility for appearance in the interview is subject to verification of documents on the interview date. Candidates can e-mail their grievances (if any) to dean.usar@ipu.ac.in.

Rahul
19/12/24
Dr. Rahul Johari
PhD Program Coordinator

Copy to:

1. Dean, USAR for kind information please.
2. Director, East Delhi Campus, for kind information please.
3. Director (DRC), GGSIPU, for kind information please.
4. In charge, UITS with request to upload the notice on University Website
5. Guard File.

Atul Tripathi
19/12/24
Dr. Atul Tripathi
PhD Program Co-Coordinator